

Overview

Position: Senior Test Engineer (ATE, Semiconductor, Mixed Signal)

Reporting to: Manager, Operations

Location: Irvine, CA

Job Description

This position has responsibility for implementing and validating production and characterization automated test solutions. Responsibilities include working with the design and product development teams to define testability, verification and characterization plans, and implementing ATE tests. The person will work with product engineering to characterize products and deliver timely and complete characterization reports with the goal of maximizing yield and outgoing quality levels for highly complex products.

Required Skills & Experience

- 5+ years semiconductor ATE experience
- Mixed signal and high frequency test experience
- Vector translation and debug skills
- Strong C/C++ coding skills
- Ability to understand complex test hardware
- Strong debug skills

In addition to the required skills, experience in the following areas would be applicable:

- RF and/or Optical
- Verilog
- 10Gb Phy device testing
- Hardware design and signal integrity experience
- Lab experience with a variety of bench test equipment

For more information, please visit our website www.clariphy.com or contact us at careers@clariphy.com.

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