

Overview

Position: Analog Engineer

Reporting to: VP Engineering

Location: Irvine, CA

Job Description

The Company seeks a full-time Analog IC Design Engineer. This person will be responsible for all activities ranging from architecture and design, to silicon bring up and characterization. The Company designs and manufactures high-speed communications integrated circuits. The candidate will develop CMOS ADC, DAC, PLL, CMU, data converters, and linear broadband amplifiers with emphasis on performance, size, and low power.

Required Skills & Experience

The candidate must have a proven track record in the design and layout of high performance, low power, high speed CMOS A/D and D/A converters, PLL's, PGA, and filters. This person must have released products to high volume production successfully. The candidate must have experience in advanced CMOS technologies such as 65nm and/or 40nm. The candidate must be capable of leading the development of a complete AFE module. Experience with 10G communication is a plus. The candidate will have 8+ years of experience.

- Low power, high performance analog circuit design
- Familiarity with Cadence environment
- Self driven, result oriented individual capable of leading a team
- MSEE required, Ph.D. degree in EE preferred.

For more information, please visit our website www.clariphy.com or contact us at careers@clariphy.com.



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