CL1012 XFP+ Evaluation Board
Product Brief

Overview

ClariPhy Communications' CL1012 MLSE-based EDC+Repeater evaluation board provides XFP+ optical module interface capabilities to the CL1012 IC in a self contained, flexible platform. The CL1012 eval board interfaces to ClariPhy's graphical user interface (GUI) on netbook for easy configuration and control.

Key Features

- XFP Socketed board for pluggable optics compatibility
- On board configurable reference clock to support common network data rates
- SMA host interface to allow simple BERT connectivity
- Operates at data rates from 9.95 to 11.4 Gb/s
- Power with AC wall adapter or external powersupply with banana jacks
- Built-in pattern generators (PRBS9/31) and BER detectors for performance monitoring
- Link monitor and diagnostics
- I2C connector for TXFP+ wavelength tuning
- Customizable applications firmware for system optimization.

Major Benefits

- Pluggable optics for multi-vendor interoperability testing
- Tunable reference clock supports data rates from 9.9Gb/s to 11.4Gb/s
- Allows easy use with common lab equipment without the need for additional splitters and cables
- Supports SONET/SDH, 10GbE and OTU2
- Flexible interfacing to common lab equipment eliminated expensive clock and data recovery equipment
- Eliminates additional pattern generator and BERT equipment
- Assists in debugging system issues
- Eliminates external power supplies
- Provides maximum flexibility in systems applications support

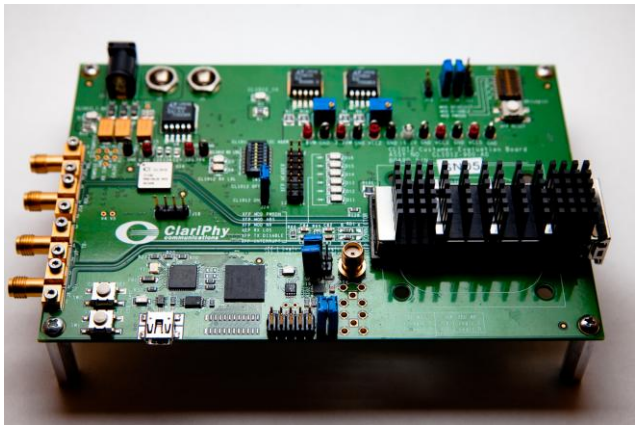

Figure 1, CL1012 Eval Board

Product Description

ClariPhy Communications' CL1012 XFP+ evaluation board is a flexible tool that allows customers to test and analyze ClariPhy's MLSE EDC technology in combination with TXFP+ tunable optical XFP module. The user has the capability to plug in any XFP MSA module and test demanding optical chanels. In addition, the user friendly GUI allows real time configuration and monitoring of FFE and MLSE parameters so that perfmormance can be observed and measured.

The evaluation board control and status is accessed through a GUI which is typically run on a netbook connected by USB. This allows the user to configure chip path options, such as data invert or RDXL output with simple point and click operation. The board runs TCL scrips which can be edited within the GUI and executed as well.

The GUI includes several panels which provide complete access to the CL1012 device. It also includes a BER panel to capture bit error rate at the receiver. This can be done for both PRBS31 and PRBS9 patterns. The GUI is show below in Figure 2.

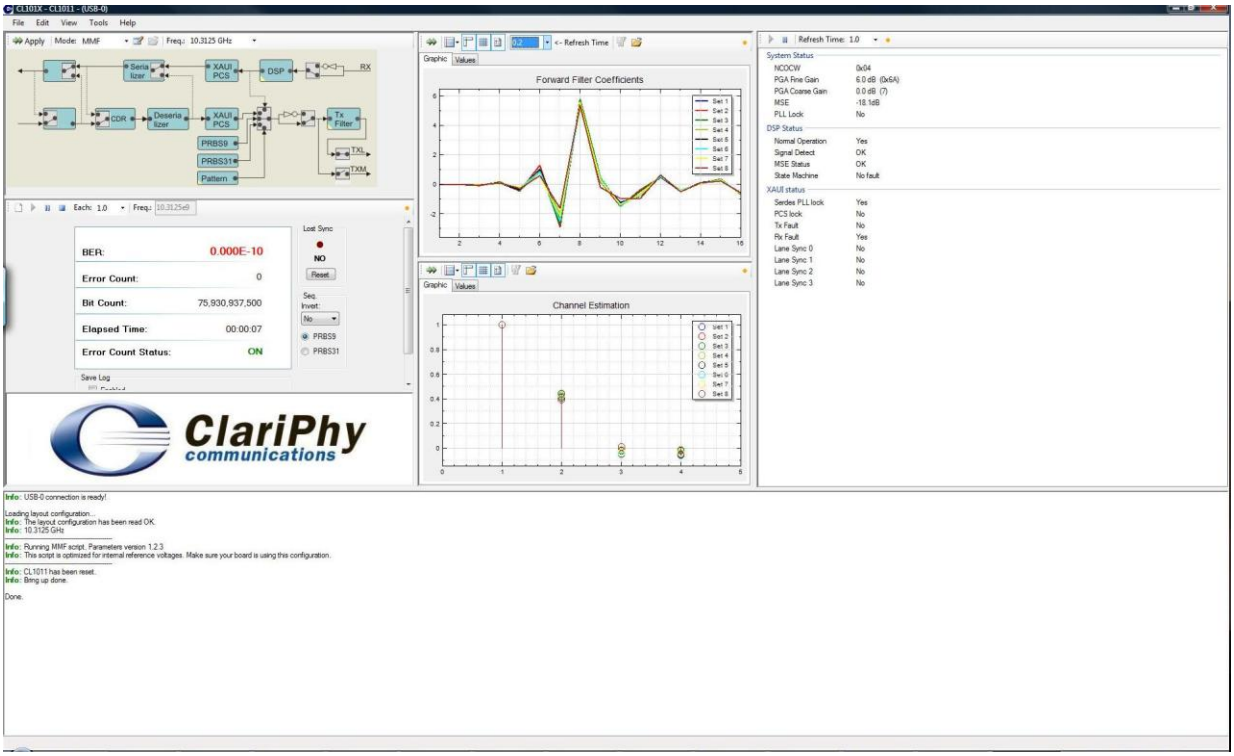

Figure 2, CL1012 Eval Board GUI

For more information on ClariPhy's products please visit our website www.clariphy.com or contact us at info@clariphy.com.

7585 Irvine Center Drive, Suite 100, Irvine, CA 92618 – phone +1 949 861-3074
www.clariphy.com